

An Analytical Framework for Computing AC Losses in the HTS Insert of the EU-DEMO Central Solenoid

(1) ENEA, Via Enrico Fermi 45, 00044 Frascati, Rome (ITALY)

(2) West Pomeranian University of Technology, Szczecin, 70-310 Szczecin (POLAND)

Superconductivity & Particle AcceleratorS (3) The Henryk Niewodniczański Institute of Nuclear Physics, Polish Academy of Sciences, 31-342 Krakow (POLAND)

INTRODUCTION - High-temperature superconductors (HTS) are being explored for integration into coil systems for magnetic confinement fusion, due to their ability to extend operational margins in terms of temperature, curren Recently, a conductor design based on the SECAS concept was proposed for the innermost layer of the central solenoid (CS) module in the EU-DEMO tokamak. The dynamic nature of plasma scenarios, characterized by rapid variat and magnetic fields, induces significant AC losses in the superconducting magnets. These losses can be particularly pronounced during phases like plasma start-up and control operation, where field variations can be signifi evaluate the instantaneous power losses — both hysteretic and coupling losses — during a baseline plasma scenario using an analytical model that accurately accounts for the temporal evolution of the magnetic field profile layers of the CS1 HTS insert. The calculated AC losses will be used in thermal-hydraulic simulations to verify if the temperature margin stays above the design threshold (ΔT_{margin} > 1.5 K), ensuring the safe operation of

THE SECTOR-ASSEMBLED CABLE - In recent research developments at ENEA [1], we have designed a novel concept for HTS sector cables, denoted as the SECtor-ASsembled cable (SECAS) based on BRAided Stacks of Tapes (BRAST). Each BRAST sub-unit comprises a stack of an arbitrary number of tapes assembled within a braid of thin, tin coated Cu wires.

IN-FIELD PROPERTIES OF REBCO COATED CONDUCTORS - Experimental data of 12-mm-wide Ic of commercial tapes at low temperatures (< 5 K) and high fields (> 10 T) are scarcely available in the literature, particularly when the field is parallel to the flat surface of the tapes. In this study, we have reconstructed the fielddependence of Ic in both parallel and perpendicular field configurations by appropriate scaling of the available experimental data under slightly different B-T conditions.

ANALITICAL AND NUMERICAL TOOLS - The instantaneous power loss of a twisted, stacked-tape cable is calculated using the theoretical expressions by Halse and Brandt [4, 5] and by London [6] for stacks subjected to an external field, perpendicular or parallel to the tape's wide surface, respectively. The analytical formulation has been extended to the general case where the field varies periodically with time and validated through finite-elements simulations.

RESULTS - We have calculated hysteresis and coupling losses in a DEMO plasma scenario [7]. A comparison was made with the formulas used in the ITER project [8]. The dominant losses are due to hysteresis, which are approximately four times higher.

 $\overline{J_c(B)}$ $\overline{J_c(B)}$ $||$ $||$ $|$ CONCLUSIONS – The instantaneous power loss of the SECAS conductor, designed by the ENEA team for the innermost layers of the hybrid CS coils of EU-DEMO, was simulated using a recently developed analytical framework. A simplified current scenario, excluding the fast breakdown phase, was considered. Our results show that hysteresis losses dominate, while coupling losses also contribute significantly during both the PCRU and CRD plasma phases. Additionally, we applied the ITER formulas using the tape width in place of the
 $\left\{\frac{(B_m + \text{sgn}(\partial_r B_\rho) B_e)^2/4B_p^3 \quad (1)}{2} \right\}$ and introduced a corrective factor to account for twisting. Future wor $\left(2\mu_0\right)^{1/2+\rho+1}$ $\left(1/\mu_0\right)^{1/2}$ $\left(2/\mu_0\right)^{1/2}$ thermo-hydraulic analyses to ensure the temperature margin remains above the design threshold. London-Bean model for infinite slab and CRD plasma phases. Additionally, we applied the ITER formulas using the tape width in place of the

REFERENCES **REFERENCES**

Twisted stack | | | [4] M. R. Halse, J. Phys. D: Appl. Phys. 3, pp. 717-720 (1970) | Research and Trainng P [1] L. Muzzi et al., IEEE Trans. Appl. Supercond. 33(5), Art no. 4200106 (2023) This work has been carried out within the [2] X. Zhang et al., Supercond. Sci. Technol. 30(2), Art. no. 025010 (2017) This work of the EUROfus [2] X. Zhang *et al.*, Supercond. Sci. Technol. **30(**2), Art. no. 025010 (2017) [3] G. Blatter *et al.*, Rev. Mod. Phys. **66**, pp. 1125-1388 (1994) **EFERENCES**

[2] Auzzi et *al.* [EEE Trans. Appl. Supercond. **33**(3), Art no. 2020/10 (2023)

[2] X. Dang *et al.* Supercond. Sci. Technol. **30**(2), Art no. 2020/10 (2023)

[2] X. Ring and *K*. Indeed by the European Unio

 $Q_c = \int dt P_c$ L_p = twist pitch $\frac{1}{2}$ Time, $t[n]$

Algoes and opinions expressed are however those of the author(s)
only and do not necessarily reflect those of the European Union or
the European Commission. Neither the European Union or
the European Commission can be held

