

The Fast Interaction Trigger Upgrade for ALICE

Sebastian Bysiak¹ on behalf of the ALICE Collaboration ¹Institute of Nuclear Physics PAN, Poland, sbysiak@cern.ch

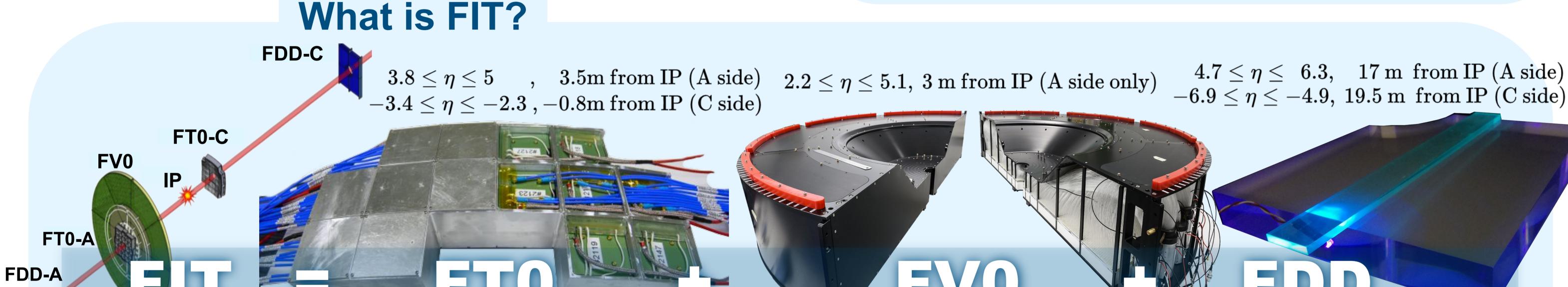
FIT is new **forward** detector of ALICE consisting of 3 modules built to make the most of the **Run3+**. It will be used for **triggering**, **beam monitoring** and determination of **centrality** and **event plane**.

Motivation

ALICE detector is undergoing a major upgrade during the second long shutdown (LS2) of the LHC. It is being prepared to collect PbPb data with increased interaction rate of 50 kHz. To make it possible, most detectors within ALICE will operate in continuous read-out mode, which require online event selection with fast and efficient forward detectors.

Tasks

- luminosity and beam monitoring, feedback to LHC
- triggering:
 - Minimum Bias trigger
 - centrality/multiplicity selection
 - online vertex determination
 - beam-gas events rejection
 - veto for ultraperipherial collisions without activity in forward direction
- centrality & event plane determination
- PID via providing time0 for TOF detector

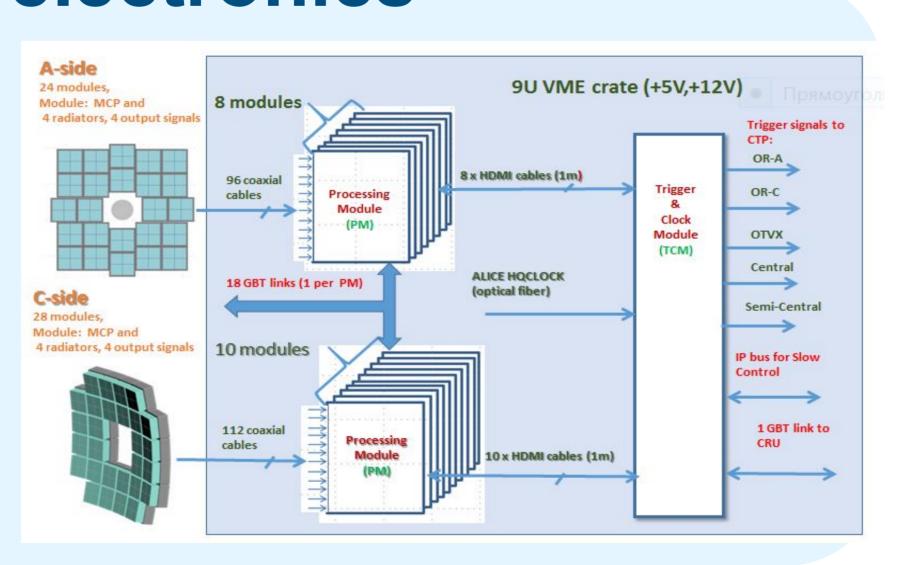


FIT replaces 3
 detectors from Run2
 setup: T0, V0, AD

- benefits from usage of the same readout electronics and better integration of subdetectors
- two arrays of 28/24
 Cherenkov modules
- radiator optically coupled to MCP-PMT (customized Planacon XP85012)
- each module split into 4 independent channels
- due to limited space, located only on one side
- large area (∅=148cm)
- plastic scintillator connected with clear fibers to 48 Hammamatsu PMTs
- split into 5 rings with equal η coverage
- 4 sectors on each side
- plastic scintillator
- → wavelength shifters
- → clear fibers
- → PMT

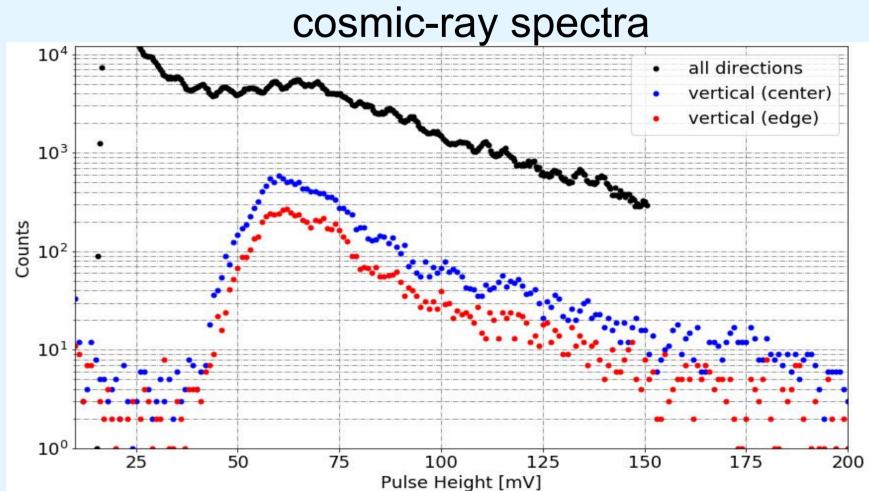
FIT electronics

all FIT detectors share common custom-made electronics boards: PM & TCM (Processing Module & Trigger and Clock Module) but each needs custom FPGA firmware

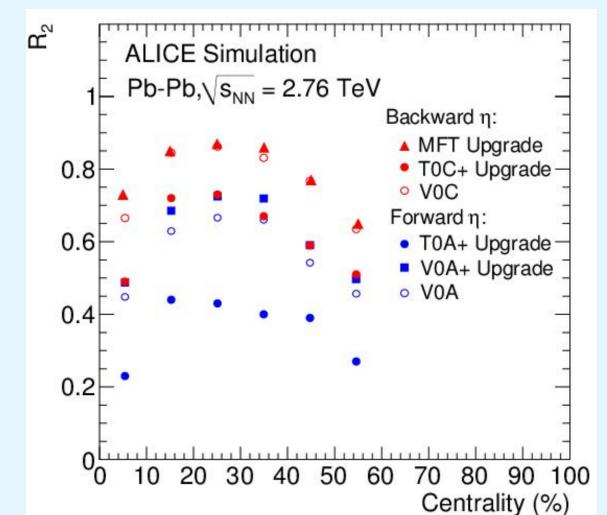


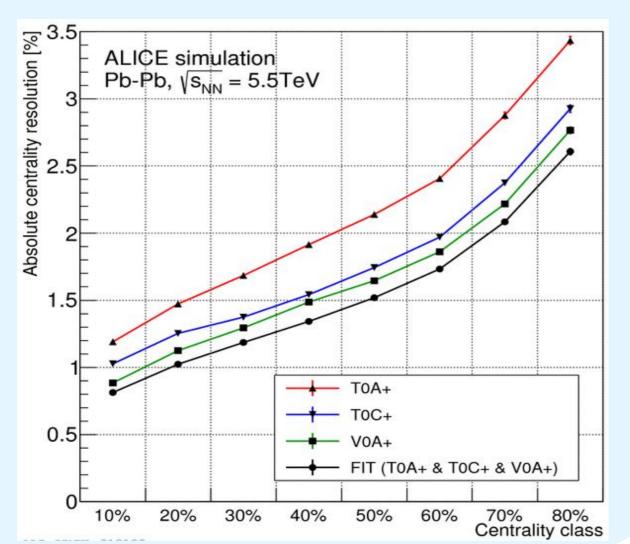
FV0 comissioning

- detector is fully assembled
- measurements with cosmic-rays and laser were performed with actual front-end electronics



Simulation performance





Trigger?